

EXAMINER'S AMENDMENT NOTE for App. No. 10/671,133

Atty Ref. No. 109263-131742

Listing of Claims -

1. (~~Amended~~Withdrawn) A socket contact, comprising:
 - a first end, ~~the first end having a simple geometry that is~~ configured to interconnect with a load-generating device; and
 - a second end, the second end having a contact pad of ~~a complex geometry that is~~ configured to enable placement and electrical coupling of an electronic component between the socket contact and a substrate and electrical coupling of the electronic component between the socket contact and a bus disposed on the substrate;

wherein the contact pad includes a first contact pad area that is curvilinear and a second contact pad area that is rectilinear.
2. (~~Withdrawn~~canceled) ~~The socket contact of Claim 1, wherein the contact pad includes a first contact pad area and a second contact pad area.~~
3. (~~Withdrawn~~canceled) ~~The socket of Claim 2, wherein the second contact pad area is rectilinear and the first contact pad area is curvilinear.~~
4. (~~Amended~~Withdrawn) The socket of Claim 21, wherein the first contact pad area is configured to couple to an interface configuration selected from a group including LGA, PGA, CSP, and BGA.
5. (~~Amended~~Withdrawn) The socket of Claim 21, wherein the second contact pad area extends in a third dimension from the first contact pad area.
6. (~~Amended~~Withdrawn) The socket of Claim 5, wherein the second contact pad area includes a pair of opposed contact pad areas configured to grippingly engage a component.
7. (~~Amended~~Withdrawn) The socket of Claim 1, wherein the electronic component is selected from a group including capacitors, resistors, diodes, and inductors.
8. (~~Amended~~Withdrawn) The socket contact of Claim 1, wherein a plurality of socket contacts are electrically and mechanically interconnected with each other.
9. (Original) A system, comprising:
 - a system substrate;
 - a bus disposed on the system substrate to facilitate data exchange;
 - ~~a memory configured to store data, the memory disposed on the system substrate and coupled to the bus; and~~
 - a socket coupled to the system substrate, the socket including:
 - a body;
 - a socket contact housed by the body, the socket contact including

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• a first end, ~~the first end having a simple geometry that is configured to interconnect with~~ a load generating device; and

a second end, the second end having a contact pad ~~of a complex geometry that is configured to enable placement and electrical coupling of an electronic component between the socket contact and a substrate and electrical coupling of the electronic component between the socket contact and the bus;~~

wherein the contact pad includes a first contact pad area that is curvilinear and a second contact pad area that is rectilinear.

10. (Original) ~~The system of Claim 9, wherein the contact pad includes a first contact pad area and a second contact pad area.~~

11. (Original) ~~The system of Claim 10, wherein the second contact pad area is rectilinear, and the first contact pad area is curvilinear.~~

12. (Original) The system of Claim ~~10~~9 wherein the first contact pad area is configured to couple to an interface configuration is selected from a group including LGA, PGA, CSP, and BGA.

13. (Original) The system of Claim ~~10~~9, wherein the second contact pad area extends in a third dimension from the first contact pad area.

14. (Original) The system of Claim 13, wherein the second contact pad area includes a pair of opposed contact pad areas configured to grippingly engage a component.

15. (Amended Original) The system of Claim 9, wherein the electronic component is selected from a group including capacitors, resistors, diodes, and inductors.

16. (Original) The system of Claim 9, wherein a plurality of socket contacts are electrically and mechanically interconnected with each other.

17. (Original) A socket connection, comprising:

a substrate;

an electronic component, the electronic component electrically coupled to the substrate;

and

a socket body, the socket body including a plurality of socket contacts, each of the socket contacts including

a first end configured to electrically couple with a load generating device, and

a second end, the second end having a contact pad configured to enable placement and electrical coupling of the electronic component between the contact second end and the substrate and electrical coupling of the electronic component between the socket contact and a bus disposed on the substrate.

18. (Original) The socket connection of Claim 17, wherein the first end is of a simple geometry and the second end is of a complex geometry.

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19. (Original) The socket connection of Claim 17, wherein the electronic component is selected from a group including capacitors, resistors, diodes, and inductors.

20. - 23. (Withdrawn) canceled